

**IC693CPU340**

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## IMPORTANT PRODUCT INFORMATION

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### READ THIS INFORMATION FIRST

**Product:** IC693 CPU Firmware, Release 7.00 for Full Production  
IC693CPU340H

This is a feature release of the IC693 PLC firmware. This release introduces a new model, the IC693CPU352, which supports floating point math operations. This release of firmware also doubles the amount of user memory available in the IC693CPU313 and IC693CPU323, from 6K bytes to 12K bytes. No new hardware is required for this increase in user program memory. Additionally, the serial ports on a CPU 351 and CPU 352 can now be configured using IC641 programming software, release 7.00. Also included in this release is the ability to use the memory protection keyswitch on the CPU 351 and CPU 352 as a RUN/STOP keyswitch.

New Catalog Number	Replaces
IC693CPU340H	IC693CPU340C,D, E, F, G

The hardware and software identification for this release is summarized in the following table.

Catalog Number	Board Identification	Board Revision	UVEPROM Label	UVEPROM Location
IC693CPU340H	CH3A2	44A735369G01R03or later	528-003J 7.00	U35

## Upgrade Information

Upgrade kits are available to upgrade existing units with 6.61 or earlier firmware to release 7.00 firmware. This upgrade is optional. Existing units may be upgraded for a charge by ordering the applicable field upgrade kit.

Upgrade Kit	For Upgrading
44A736856-G06	IC693CPU340Gor earlier versions

## Functional Compatibility

IC641 programming software version 7.00 or later must be used to take advantage of the new features.

## Packaging Note

The user manual is not shipped in the box with every product. User manuals are provided as a complete set in a library with IC641 Programming Software products, are available on CD ROM, or can be ordered as individual manuals.

## Documentation

The following table lists the applicable documentation for the model 341 CPU. Documentation is also available on CD ROM, catalog number IC690CDR002\*. (\* current version will be shipped).

Catalog Number	User Manual
IC693CPU340H	ProgrammableController Installation Manual (for IC693 Products)

## Operational Notes

### UVEPROM Change

1. The User Program, configuration, CPU ID (used for SNP communications), and status tables will automatically be cleared when the CPU firmware in UVEPROM or flash memory, as applicable, is changed. You will need to restore these if upgrading from a previous CPU version. The user program, configuration, and status tables can be restored from an IC641 programming software folder, or from memory card (except on CPU 351 / 352), EEPROM, or flash memory. The SNP ID must be set separately, using IC641 programming software or the Hand-Held Programmer.

### Ethernet Interface Module Compatibility

2. All IC693 Ethernet Interface (IC693CMM321) modules used with release 6.5 or later of the IC693 PLC should be updated to release 1.10 or later of the IC693CMM321. IC641 TCP/IP Ethernet software requires both CPU release 6.5 or later and IC693CMM321 release 1.10 or later.

During a Run Mode (Alt-s) Program Store of a large program block (greater than 14 Kbytes), the Ethernet Interface module may timeout, causing communications to fail. Changing the Communication Window to Run-to-Completion mode, or storing the program in stop mode, will allow the Store to take place successfully.

### Battery-Less Operation

3. When using an IC693CPU340, IC693CPU341, or IC693CPU351 CPU in a battery-less system, a standard 0.1 *berg* jumper should be installed across either of the two power supply battery connectors to ensure proper operation of the CPU. This jumper should not be installed if a battery is plugged into either the power supply or CPU battery connector.

### GCM Compatibility

4. Board revision R08 or later of the IC693CMM301 should be used with the IC693CPU352.

### Retentive Warning

5. The *USE WRN* message may be displayed inappropriately by the Hand-Held Programmer. The program check function can be used to determine if a coil is being used multiple times.

## New Features and Functionality

### Floating Point Operations - CPU 352

1. This release introduces a new model, the IC693CPU352, which supports floating point math operations.

### User Memory Increased - CPU 313 and CPU 323

2. The amount of user memory available in the IC693CPU313 and IC693CPU323, has been increased from 6K bytes to 12K bytes. No new hardware is required for this increase in user program memory.

### Serial Port Configuration - CPU 351 and CPU 352

3. The serial ports on a CPU 351 and CPU 352 can now be configured using IC641 programming software, release 7.00.

### Memory Protect Keyswitch Functionality - CPU 351 and CPU 352

4. Also included in this release is the ability to use the memory protection keyswitch on the CPU 351 and CPU 352 CPUs as a RUN/STOP keyswitch.

## Problems Resolved by This Release

### ALT-S in Stop Mode

1. When using Alt-s in run mode, program memory became fragmented, and a resulting Alt-s in stop mode may not have worked. This problem has been fixed in release 7.00.

### HHP Overrides

2. Override bits set with the Hand-Held Programmer would not be displayed correctly beyond the first byte of data. This problem has been fixed in release 7.00.

### HHP Coil Use

3. Multiple Coil Use detection when the conflicting coil use was outside of the main block was not previously detected in release 6.61. This problem is now fixed in release 7.00.

### HHP Subroutines

4. Editing subroutines with the Hand-Held Programmer in release 6.61 could result in a software fault. This problem has been fixed in release 7.00.

## Restrictions and Open Problems

None

## **Changes and Additions to the User's Manual**

The following will be added to a future version of the Hand-Held Programmer User's Manual.

If the SFC language is used to create a program, then the Hand-Held Programmer may not be used to edit this program, or to change the Coil Use setting while the SFC program is stored in the PLC.

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